

Docket No.: 67161-140

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
	:	
Naoya WATANABE, et al.	:	Confirmation Number:
	:	
Serial No.: Divisional of Appl. No.	:	
10/077,833	:	Group Art Unit:
	:	
Filed: April 13, 2004	:	Examiner:
	:	
For: SEMICONDUCTOR MEMORY DEVICE	:	

INFORMATION DISCLOSURE STATEMENT

Mail Stop Information Disclosure
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The references were cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 10/077,833, filed February 20, 2002, which is relied upon for an

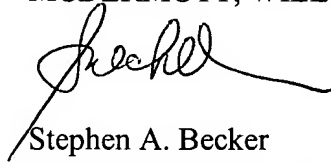
Divisional of Appl. No. 10/077,833

earlier filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR 1.98(d).

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

A handwritten signature in black ink, appearing to read "Becker", with a long horizontal flourish extending to the right.

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION				ATTY. DOCKET NO. 67161-140		SERIAL NO. Divisional of Appl. No. 10/077,833	
(PTO-1449)				APPLICANT Naoya WATANABE, et al.			
				FILING DATE April 13, 2004		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ₂ (<i>if known</i>)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		US	5,761,148	06/02/1998	Allan et al.		
		US	5,875,149	02/23/1999	Oh et al.		
		US	6,111,808	08/29/2000	Khang et al.		
		US	5,986,964	11/1999	Ariki et al.		
		US	6,366,515	04/2002	Hidaka		
		US	6,477,108	11/2002	Arimoto et al.		
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FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number 4 -Kind Codes (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		JP 10-050958	02/20/1998				
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		"Figure 9 SDRAM Functional Block Diagram", IEEE Micro, November/December 1997, pp. 38-39.					
		"A Configurable DRAM Macro Design for 2112 Derivative Organizations to be Synthesized Using a Memory Generator", YABE et al., 1998 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, pp. 72-73.					
EXAMINER				DATE CONSIDERED			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.